

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
6 November 2003 (06.11.2003)

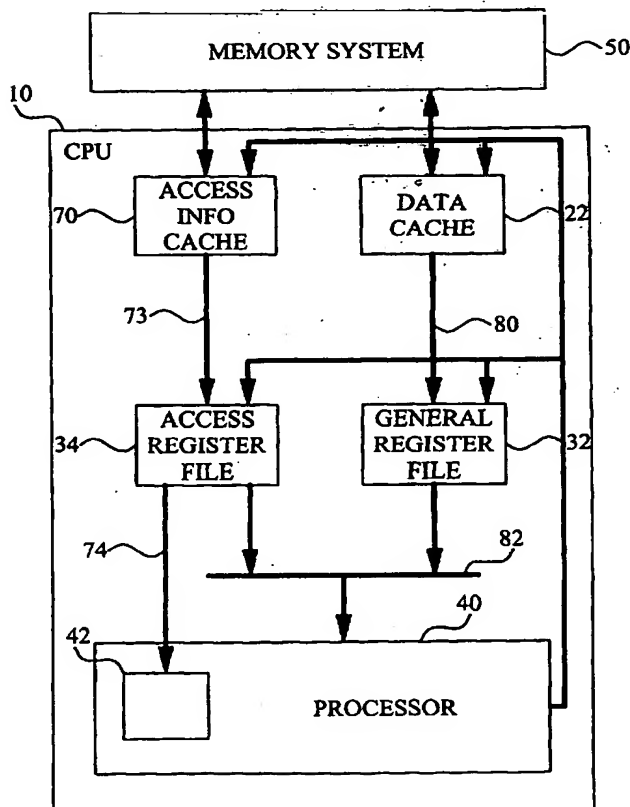
PCT

(10) International Publication Number
WO 03/091972 A1

- (51) International Patent Classification⁷: **G09F 9/30, G06F 9/32**
- (21) International Application Number: **PCT/SE02/00835**
- (22) International Filing Date: **26 April 2002 (26.04.2002)**
- (25) Filing Language: **English**
- (26) Publication Language: **English**
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- (81) Designated States (national): **AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZM, ZW.**
- (84) Designated States (regional): **ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).**

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(54) Title: **MEMORY ACCESS REGISTER FILE**



(57) Abstract: The general idea according to the invention is to introduce a special-purpose register file (34) adapted for holding memory address calculation information received from memory (50, 70) and to provide one or more dedicated interfaces (73, 74) for allowing efficient transfer of memory address calculation information in relation to the special-purpose register file. The special-purpose register file (34) is preferably connected to at least one functional processor unit (42), which is operable for determining a memory address based on memory address calculation information received from the special-purpose register file (34). Once the memory address has been determined, the corresponding memory access can be effectuated.